



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech. in Electrical and Electronics Engineering**


**SEMESTER-III (2023-2027)**


S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEC301	Advanced Programming Concepts	2	0	4	4	60	20	20	30	20	150
2	-	Generic Elective I	4	0	0	4	60	20	20	0	0	100
3	BTEC302	Network Analysis and Synthesis	3	1	2	5	60	20	20	30	20	150
4	BTEC303	Electronic Measurement and Instrumentation	3	0	2	4	60	20	20	30	20	150
5	BTEC304	Electronic Devices and Circuits	3	0	2	4	60	20	20	30	20	150
6	BTEE105	Basics of MATLAB	0	0	4	2	0	0	0	30	20	50
<b>TOTAL</b>			<b>15</b>	<b>1</b>	<b>14</b>	<b>23</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>150</b>	<b>100</b>	<b>750</b>

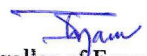
Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.


  
Chairperson  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Chairperson  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Controller of Examinations  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Vice Chancellor  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

  
Vice Chancellor  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore  
11.9.2024